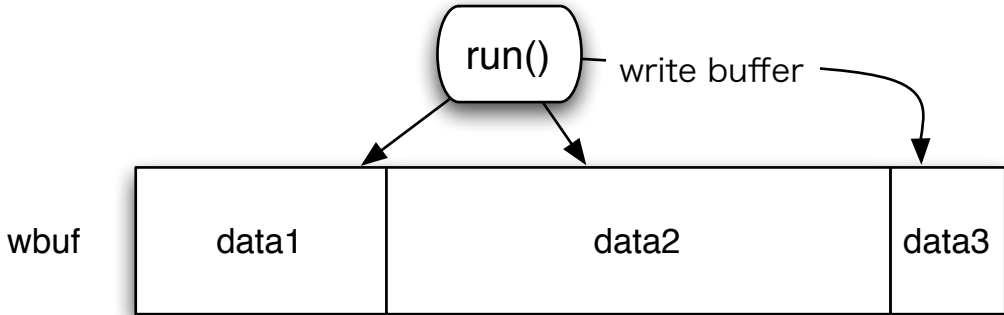


```
task->add_outData(data1, size1);
task->add_outData(data2, size2);
task->add_outData(data3, size3);
```



task finish



DMA write

DMA write

DMA write

address:  
data1

address : data2

address:  
data3

MainMemory

